

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	4087436
<b>Application Number:</b>	09987978
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	7905
<b>Title of Invention:</b>	LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF AND PROCESS FOR FABRICATING THE TWO
<b>First Named Inventor/Applicant Name:</b>	Yujiro Kajihara
<b>Customer Number:</b>	20457
<b>Filer:</b>	Paul J. Skwierawski/Stacey Keaton
<b>Filer Authorized By:</b>	Paul J. Skwierawski
<b>Attorney Docket Number:</b>	1374.32049RV1
<b>Receipt Date:</b>	09-OCT-2008
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$180
RAM confirmation Number	76
Deposit Account	
Authorized User	

### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)

1	Information Disclosure Statement Letter	32049RV1IDS.pdf	59966 9e1e676629313ab94ee184826b6715fcf1ec 1441	no	2
<b>Warnings:</b>					
<b>Information:</b>					
2	Information Disclosure Statement (IDS) Filed (SB/08)	32049RV1SB08.pdf	130221 e59b0645aabd232cf182ccda57258d2089 92e24	no	1
<b>Warnings:</b>					
<b>Information:</b>					
This is not an USPTO supplied IDS fillable form					
3	Information Disclosure Statement Letter	32049OA.pdf	333994 2f1849138e6f570c20e8a4959c727568dd09 1116	no	5
<b>Warnings:</b>					
<b>Information:</b>					
4	Foreign Reference	32049REF.pdf	268195 743c5461f471aa3bf12cf53250ecd273d96 e2a8	no	8
<b>Warnings:</b>					
<b>Information:</b>					
5	Fee Worksheet (PTO-06)	fee-info.pdf	31008 ee31810361191bbd69ace78f42bb30f50b0 cb5d4	no	2
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>				823384	
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